

FIG. 1

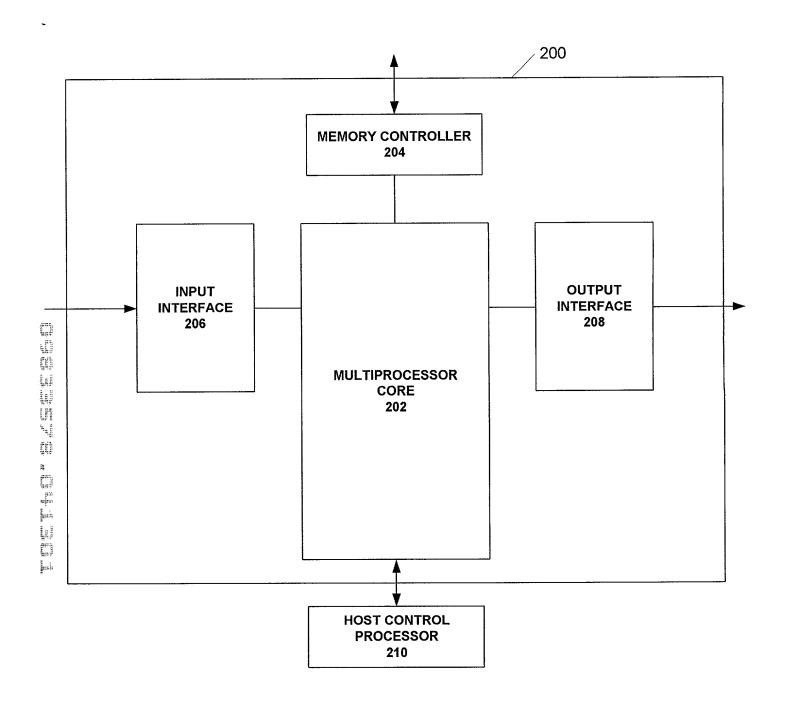
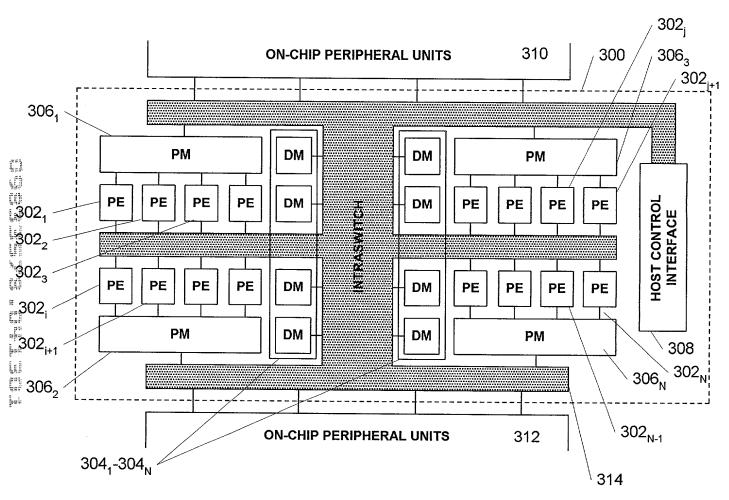


FIG. 2



PM: PROGRAM MEMORY DM: DATA MEMORY

FIG. 3 PE: PROCESSING ELEMENT

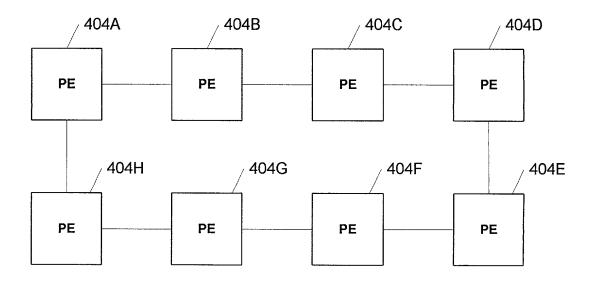


FIG. 4A

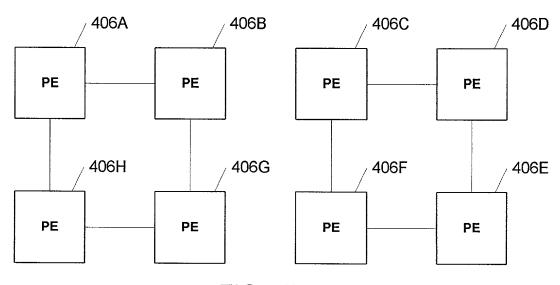


FIG. 4B

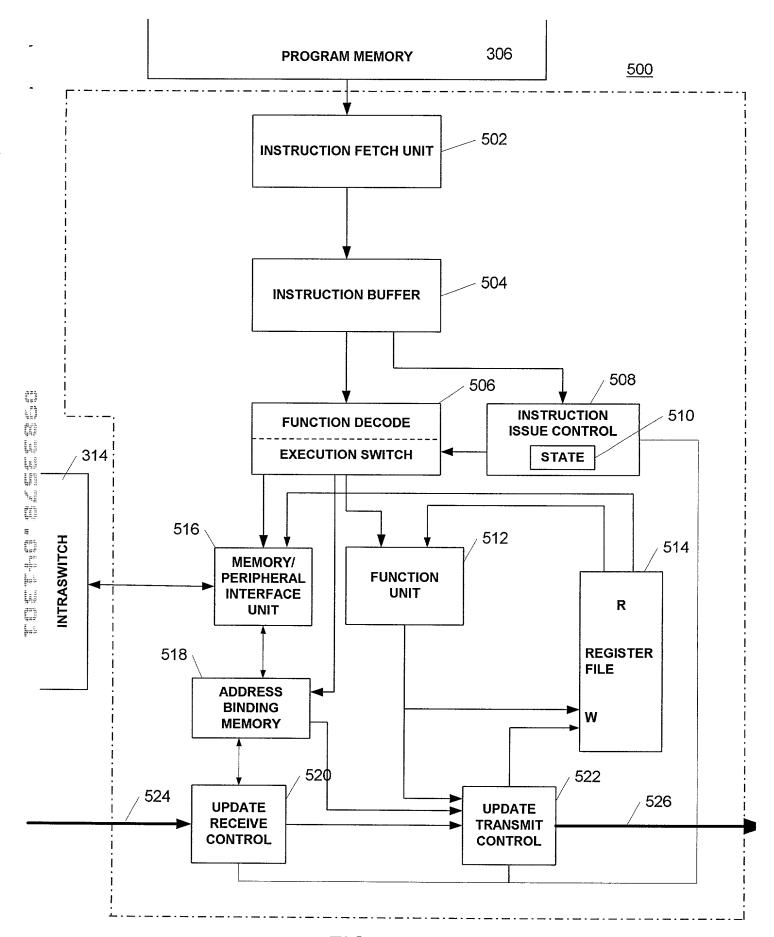


FIG. 5

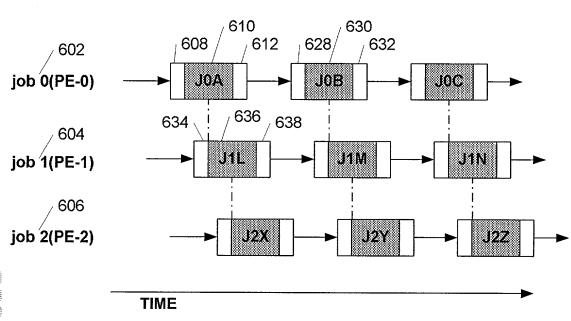


FIG. 6

grass, parts, gatts, verts, verts, parts, pa

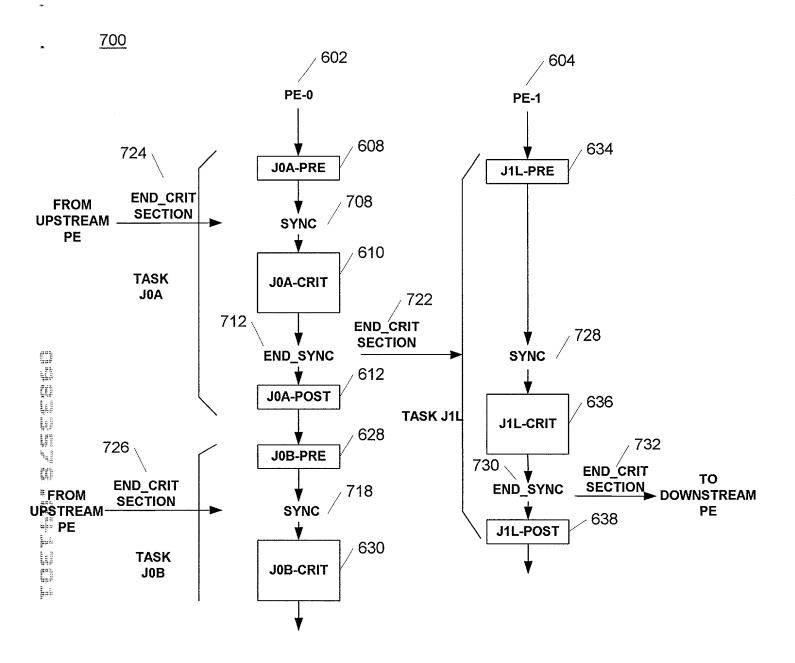


FIG. 7

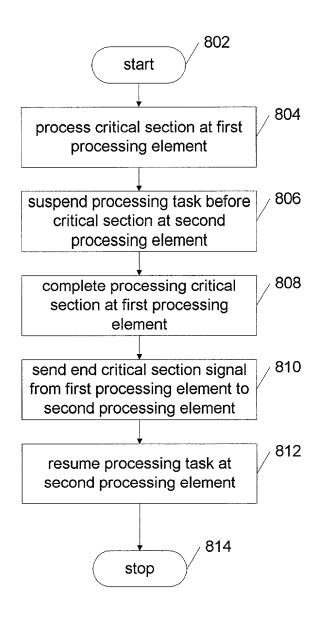


FIG. 8

<u>900</u>

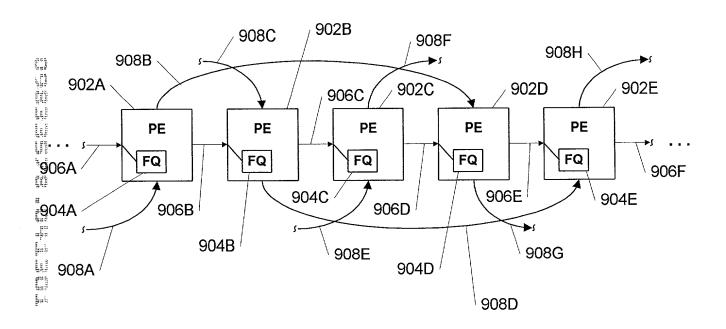
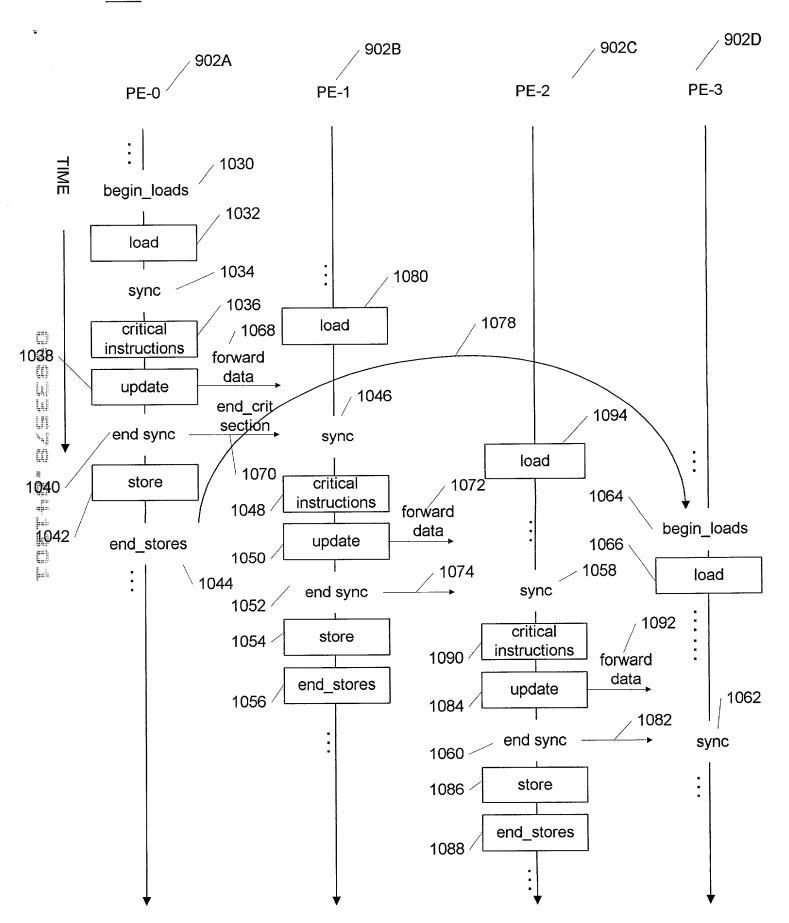
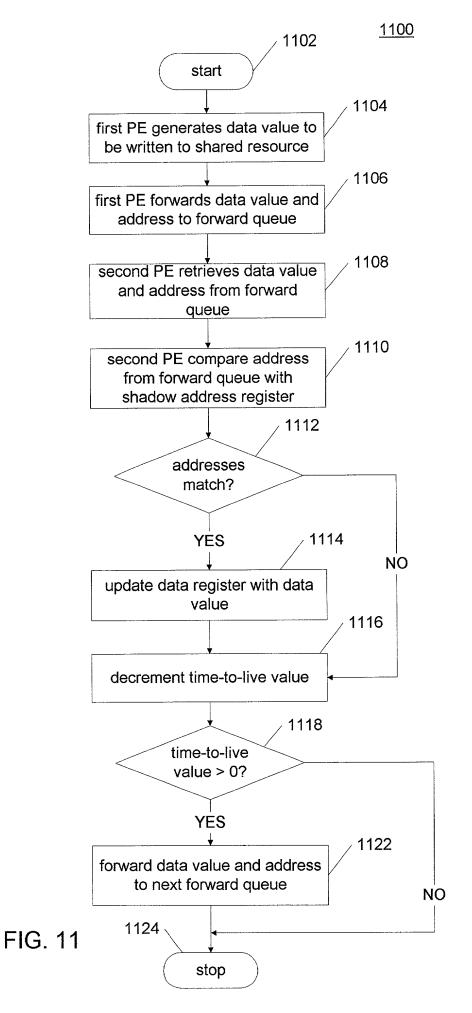


FIG. 9

1000





<u>1200</u>

	120	2 / 12	04 1206	12	08 1210
1212 \	DATA	ADDRESS	REGISTER#	TTL	LAST UPDATE
\sim	D1	A1	R1	3	F
	D2	A2	R2	2	F
	D3	A3	R3	4	F
	D4	A4	R4	1	Т

FIG. 12

<u>1300</u> 1316 1312 1314 1310 1302 1304 1306 1308 opcode endstores beginloads source 1 destination update sync source 0

FIG. 13